SECOND ANNOUNCEMENT AND CALL FOR PAPERS

Sponsored by
the IEEE Solid-State Circuits Society and the Japan Society of Applied Physics
In Cooperation with the Institute of Electronics, Information and Communication Engineers
and the IEEE Electron Devices Society

2004 SYMPOSIUM ON VLSI CIRCUITS

**Hilton Hawaiian Village
Honolulu, HI
June 17 - 19, 2004**

The 2004 Symposium on VLSI Circuits will provide the designers of integrated circuits an opportunity to meet and present important new work at an international forum. The three day Symposium will continue the successful format and atmosphere established by the previous meetings, and partially overlap the 2004 Symposium on VLSI Technology, which is being held at the same hotel.

The scope of the Symposium covers all aspects of VLSI circuits, such as:
* digital, analog, and mixed digital-analog circuits: such as processors, ASICs, A/D and D/A converters, interface circuits, etc.
* memory circuits: such as static memory, non-volatile memory, cache memory, dynamic memory, and new concepts in memories based on quantum mechanical effects, magnetism, polymers, etc.
* system architecture, circuits and building blocks for networking applications
* circuits, functional blocks using quantum dots, MEMS, sensors, etc.
* circuits for wireless and wireline communications

The emphasis will be on circuit design. Papers will be considered on the basis of originality and quality. Implementation in a semiconductor chip and measured results are not mandatory: they are required only when feasibility of the presented circuit is not clear by simulations alone, as in the case of most analog submissions. It is understood that papers in new areas are likely to contain less quantitative evaluations than those in more established areas.

SUBMISSION OF PAPERS

The complete paper for the Circuits Symposium must occupy either two or four (never three) pages including illustrations. The choice of paper length (either 2 or 4 pages) is an option for authors whenever two pages are not enough to completely describe the major novelty and results of their work. The paper length will not be a criterion in selection of the papers for the program.

This year the VLSI Symposia will offer online submission. Instructions for submitting online are available on the website (see web address below). For hard copy submissions, those interested in submitting papers should contact the secretariat, as early as possible, to receive an author's kit. ***The papers are to be submitted in final form and, if accepted, will be published as submitted.*** For hard copy submission, the paper and 52 copies are to be sent to Widerkehr and Associates (North America and Europe) or to Business Center for Academic Societies Japan (all countries except North America and Europe).

BEST STUDENT PAPER AWARD

The VLSI Circuit Symposium has established a Best Student Paper Award starting with the year 2004. The selection will base upon the quality of the written paper and the presentation. The student that receives the Best Student Paper Award will receive a monetary award and a certificate, which will be presented at the opening session of the 2005 Symposium. The Symposium will cover the travel expense and registration fee for the award recipient to attend the 2005 Symposium. The student must be the leading author and presenter of the paper, and must indicate in the submission cover letter that this is a student paper in order to be considered for this award.

Secretariat for VLSI Symposia (USA)
Widerkehr and Associates
16220 S. Frederick Ave., Suite 312
Gaithersburg, MD 20877, USA
Tel: +1-301-527-0900 ext.103
Fax: +1-301-527-0994
E-mail: vlsi@vlsisymposium.org

Secretariat for VLSI Symposia (Japan)
c/o Business Center for Academic Societies Japan
5-16-9 Honkomagome, Bunkyo-ku
Tokyo 113-8622, Japan
Tel: +81-3-5814-5800
Fax: +81-3-5814-5823
E-mail: vlsisymp@bcasj.or.jp

To improve the probability for acceptance of a submitted paper, the paper should describe original work with specific results from experiments or simulations. **The content of the paper must not be announced or published prior to the Symposium.**

Deadline for Receipt of Papers is January 7, 2004

INFORMATION AND REGISTRATION

Prospective attendees can obtain further information and forms for Symposium registration and hotel reservations by contacting their respective secretariats. Submissions from universities are encouraged. Partial travel expense support for students who are presenting papers is available upon request.

WORLD WIDE WEB

The VLSI Symposia have a web page. It contains general information on the Symposia. The address is:
http://www.vlsisymposium.org

VLSI CIRCUITS SHORT COURSE

A one-day short course will be held on June 16, 2004. Details will be given in the VLSI Circuits Symposium Advance Program, which will be issued toward the end of March, 2004.